

This Application is a Continuation In Part of commonly assigned, copending U.S.
Application No. 10/197,071, A MULTI-CONFIGURATION GPU INTERFACE
NOW U.S. PATENT No. 6,797,998

an

5 DEVICE, by Thomas E. Dewey, et al., which is incorporated herein in its entirety

AN INTERFACE DEVICE INCLUDING A VIDEO BIOS COMPONENT

10 FIELD OF THE INVENTION

The field of the present invention pertains to electronic integrated circuits. More particularly, the present invention relates to printed circuit board interfaces.

BACKGROUND OF THE INVENTION

15 Computer system manufacturers often employ multiple configurations of processors, memory, and motherboards. The different configurations are used to satisfy the different requirements of varying market segments. For example, more powerful systems can include two or more processors and have provisions for a large amount of installed RAM. Similarly, more specialized computer systems can include specialized
20 provisions for special function integrated circuit components, such as graphics accelerators, media processor chips, high-speed input output chips, and the like.

As new configurations of processors, memory, and the like are introduced, computer system manufacturers need to ensure the correct chip set configuration and